

RJP6065DPM

Silicon N Channel IGBT High Speed Power Switching

Rev.1.00

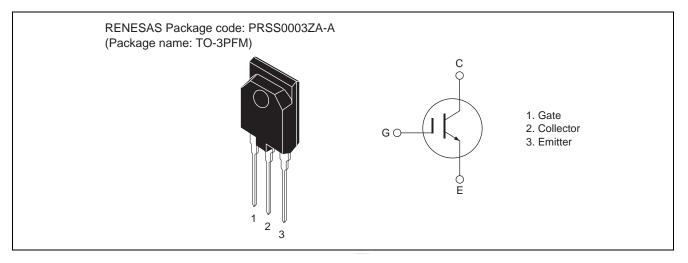
Nov 19, 2010

R07DS0204EJ0100

Features

- Low collector to emitter saturation voltage $V_{CE(sat)} = 1.8 \text{ V typ.}$ (I_C = 40 A, V_{GE} = 15V, Ta = 25°C)
- Gate to emitter voltage rating $\pm 30 \text{ V}$
- Pb-free lead plating and chip bonding

Outline



Absolute Maximum Ratings

			$(Tc = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Collector to emitter voltage	V _{CES}	630	V
Gate to emitter voltage	V _{GES}	±30	V
Collector current	Ic Note1	40	A
Collector peak current	ic(peak) ^{Note1}	100	A
Collector dissipation	Pc	50	W
Junction to case thermal impedance	өј-с	2.5	°C/W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	С°

Notes: 1. Pulse width limited by safe operating area



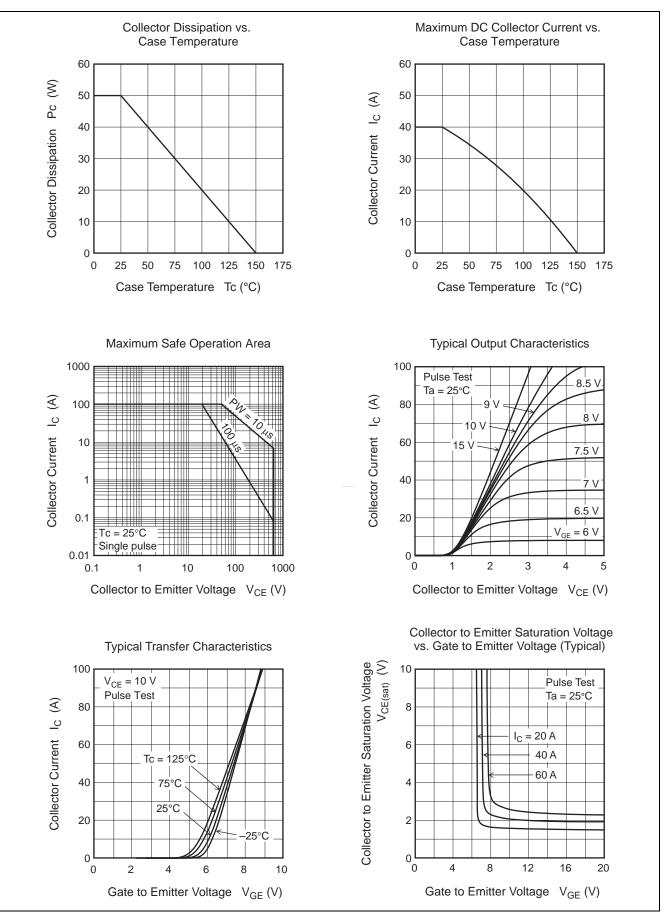
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current	I _{CES}	—	_	1	μA	$V_{CE} = 630 \text{ V}, V_{GE} = 0$
Gate to emitter leak current	I _{GES}	—	_	±100	nA	$V_{GE} = \pm 30 \text{ V}, \text{ V}_{CE} = 0$
Gate to emitter cutoff voltage	$V_{\text{GE(off)}}$	3	_	5.5	V	$V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.8	2.3	V	$I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note2}}$
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.5	_	V	$I_{C} = 20 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note2}}$
Input capacitance	Cies	—	1130	_	pF	V _{CE} = 25 V
Output capacitance	Coes	_	95	—	pF	V _{GE} = 0 f = 1 MHz
Reveres transfer capacitance	Cres	—	10	_	pF	
Switching time	t _{d(on)}	_	40	_	ns	$I_{\rm C}$ = 40 A, Resistive Load
	tr	_	90	_	ns	R _L = 7.5 Ω
	t _{d(off)}	_	80	_	ns	V _{GE} = 15 V
	t _f	—	450	—	ns	Rg = 5 Ω

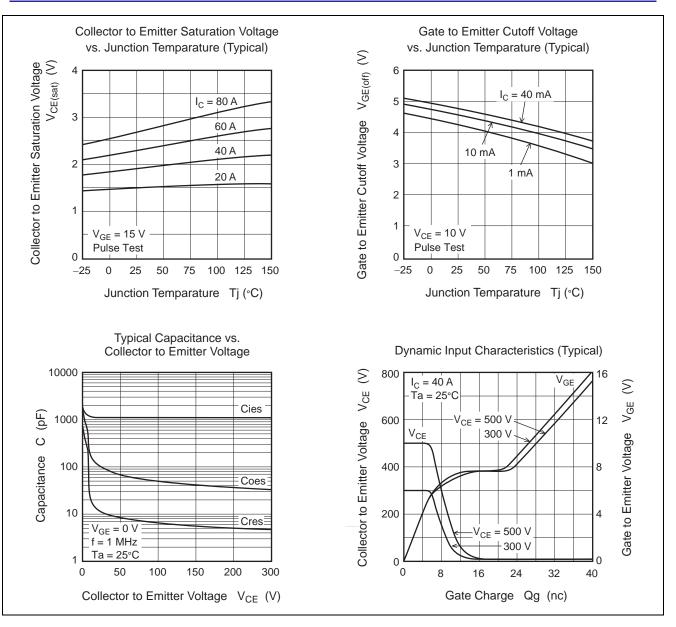
Notes: 2. Pulse test



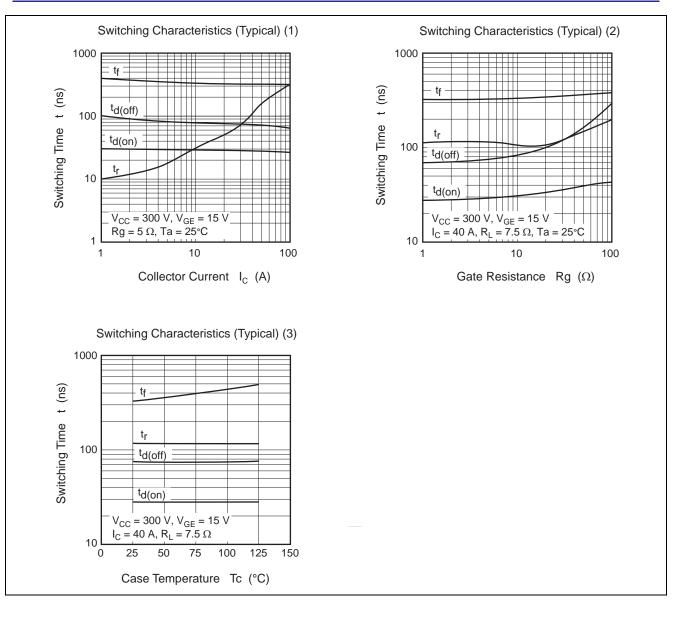
Main Characteristics



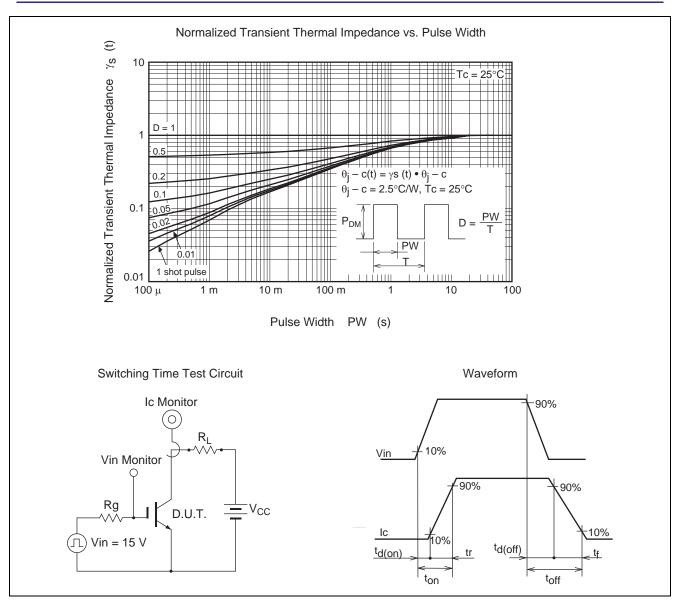






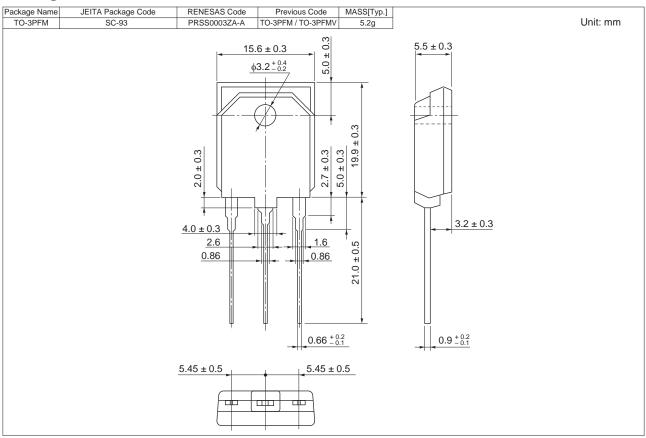








Package Dimensions



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJP6065DPM-00-T1	360 pcs	Box (Tube)



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